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FIG. 1

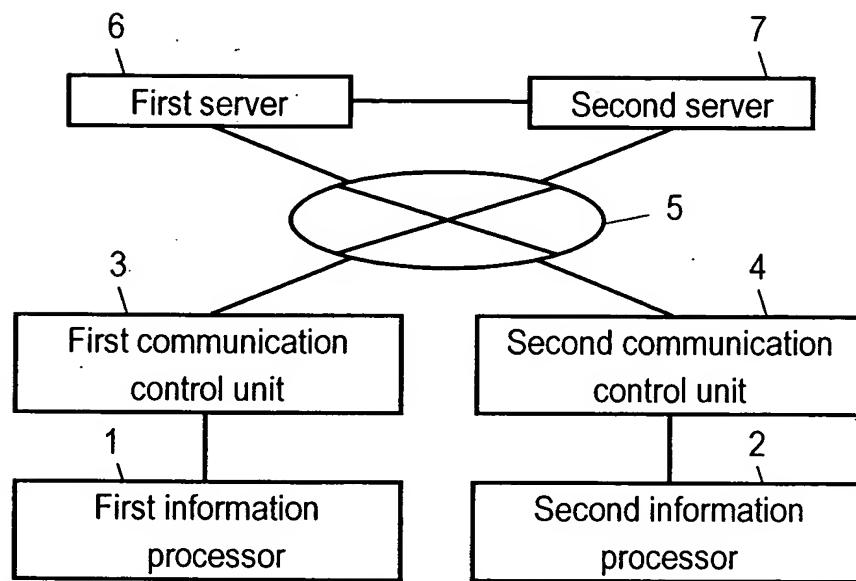
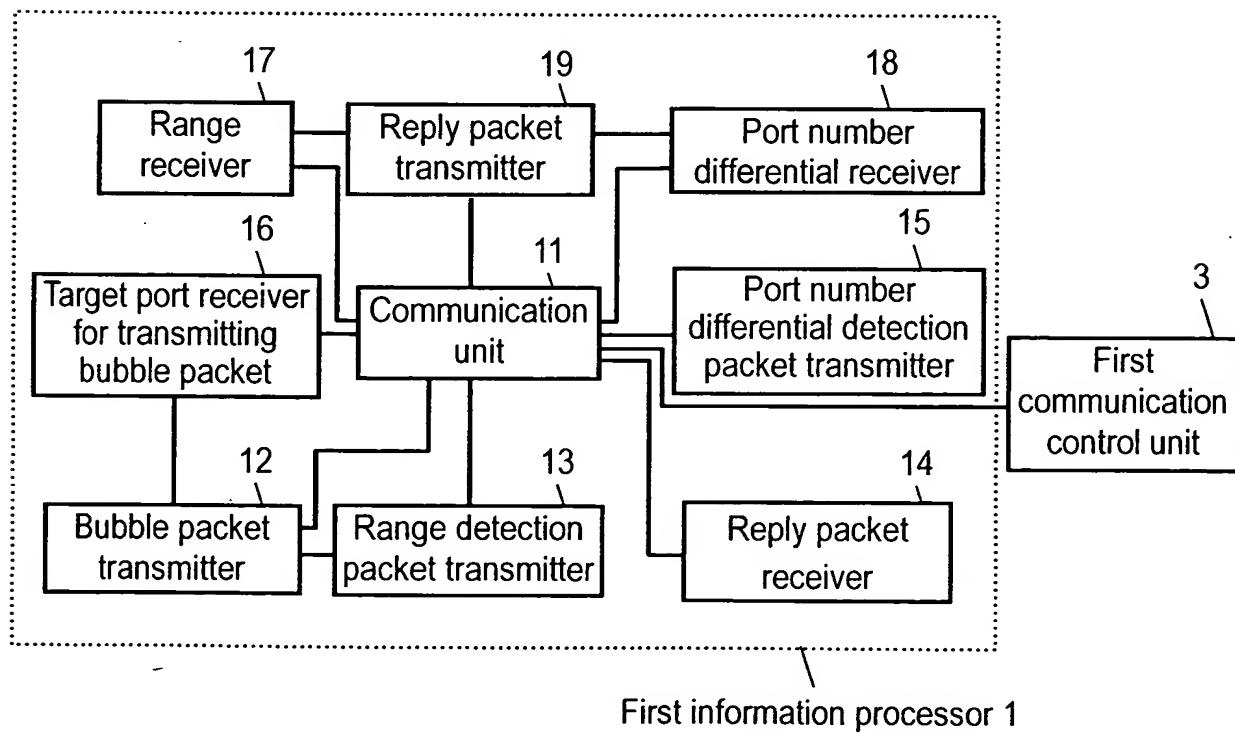
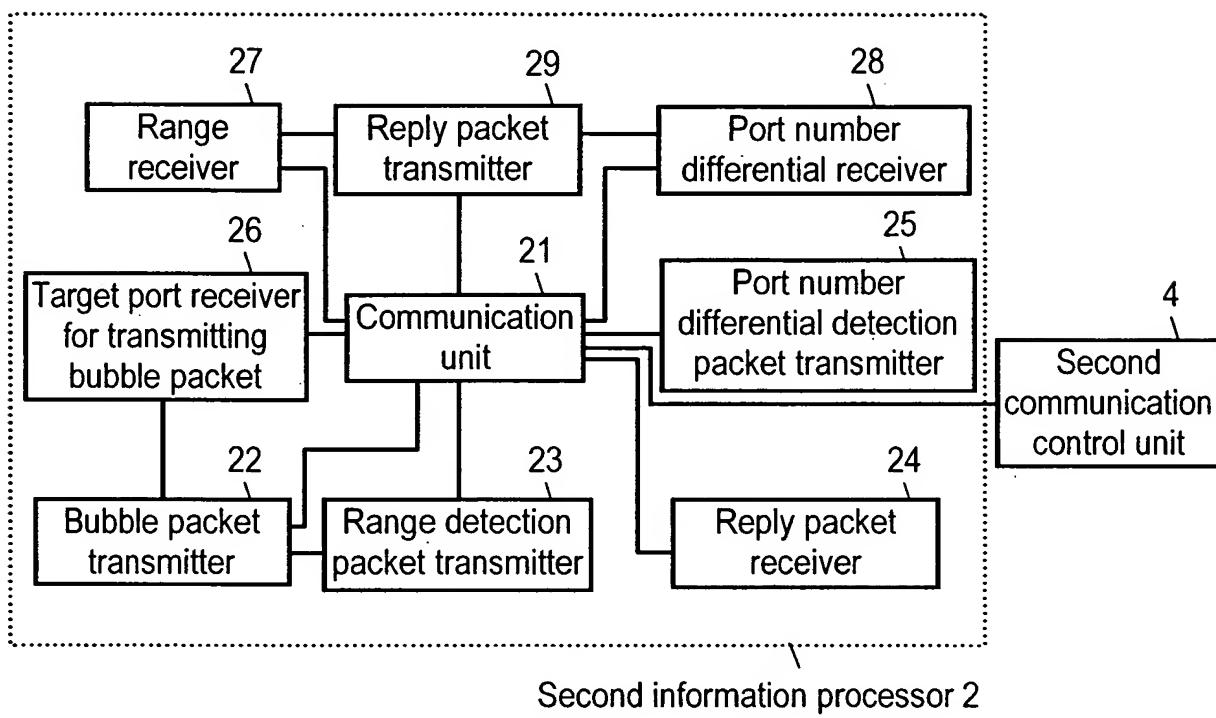


FIG. 2



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FIG. 3



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FIG. 4

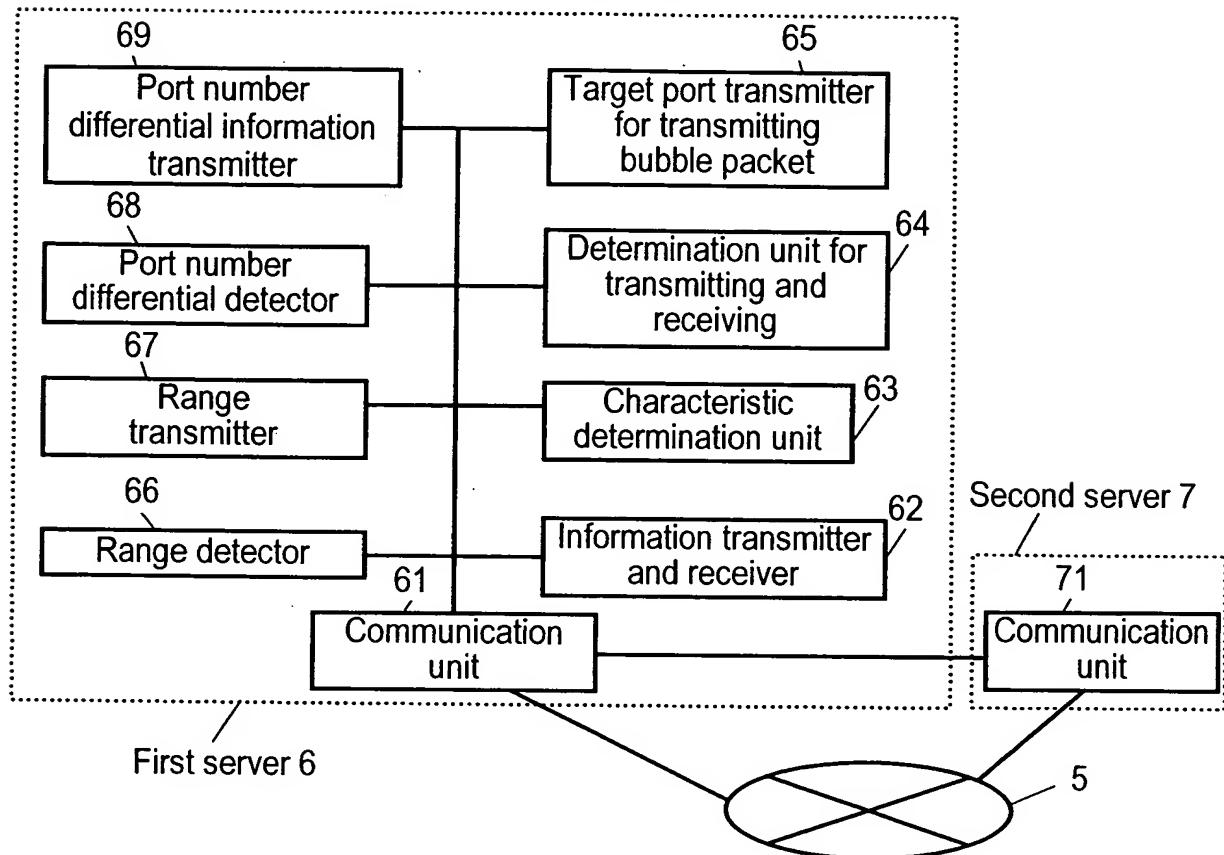


FIG. 5

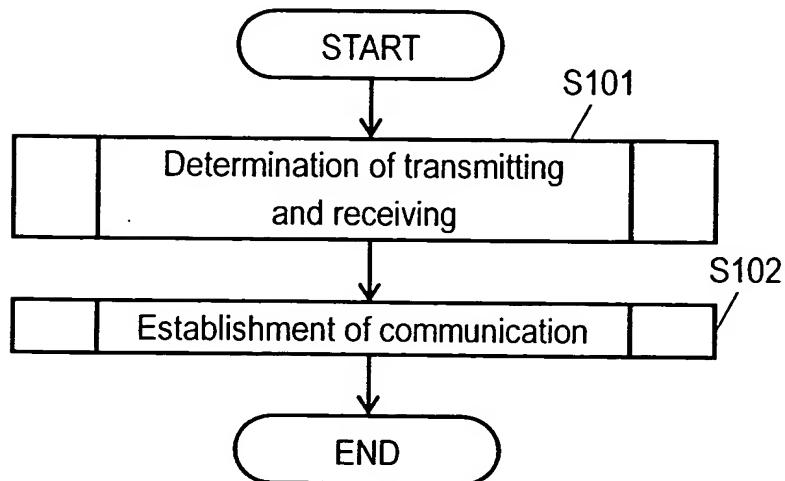
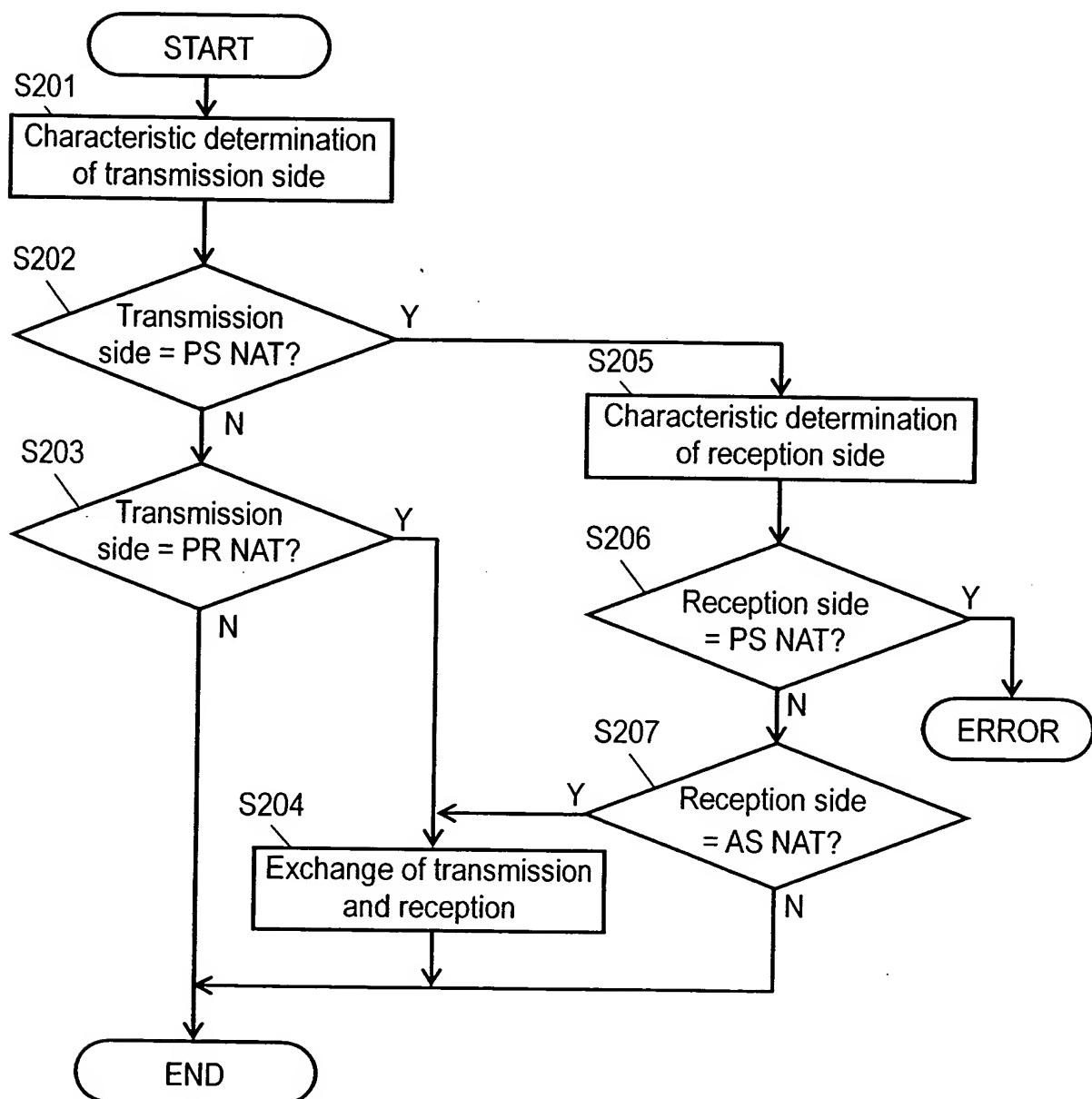


FIG. 6



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FIG. 7

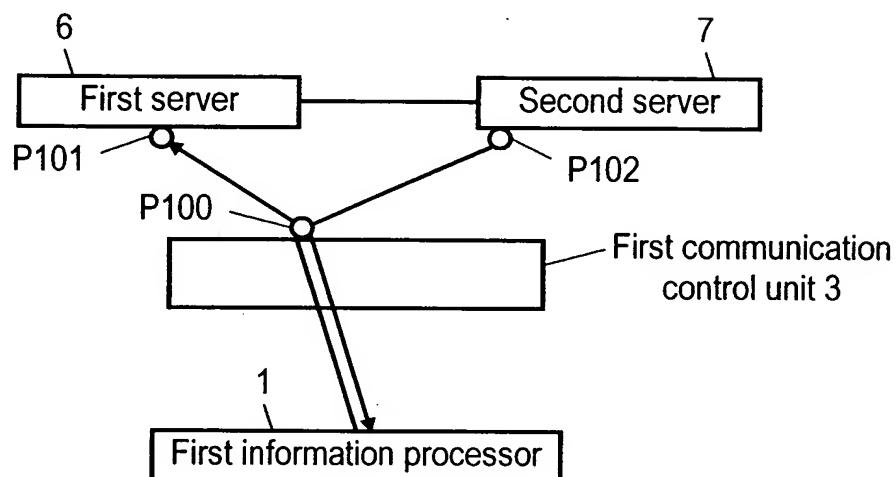


FIG. 8

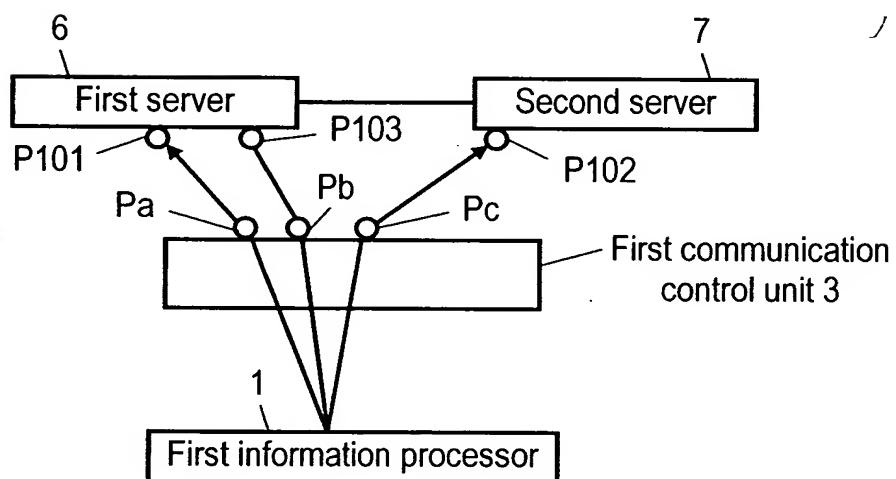


FIG. 9

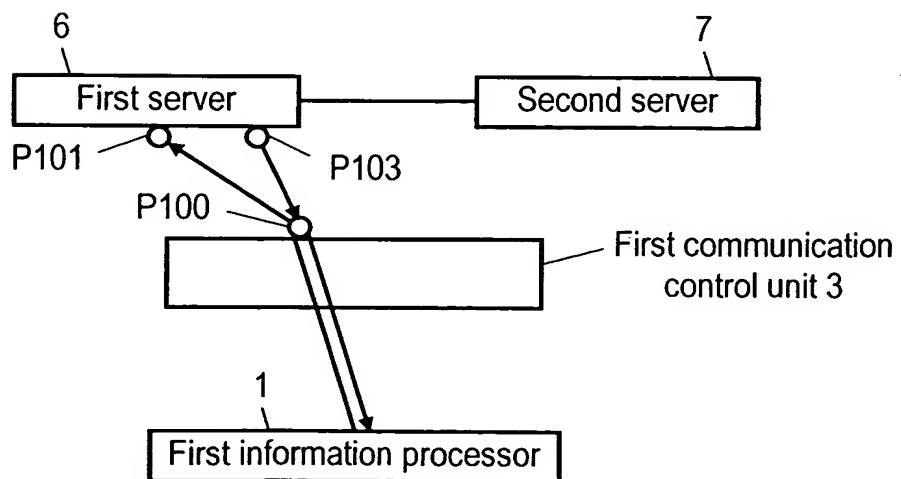
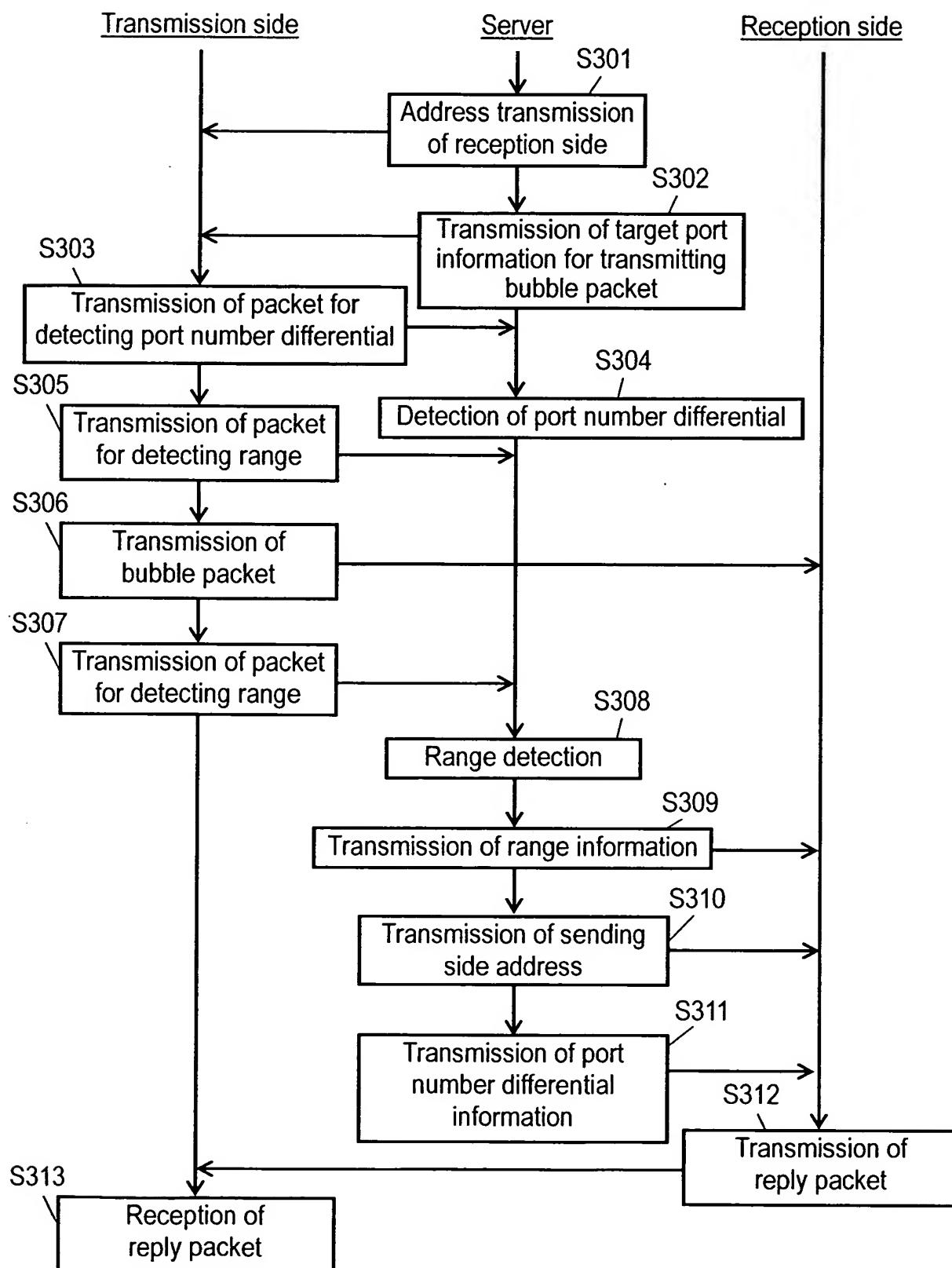


FIG. 10



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FIG. 11

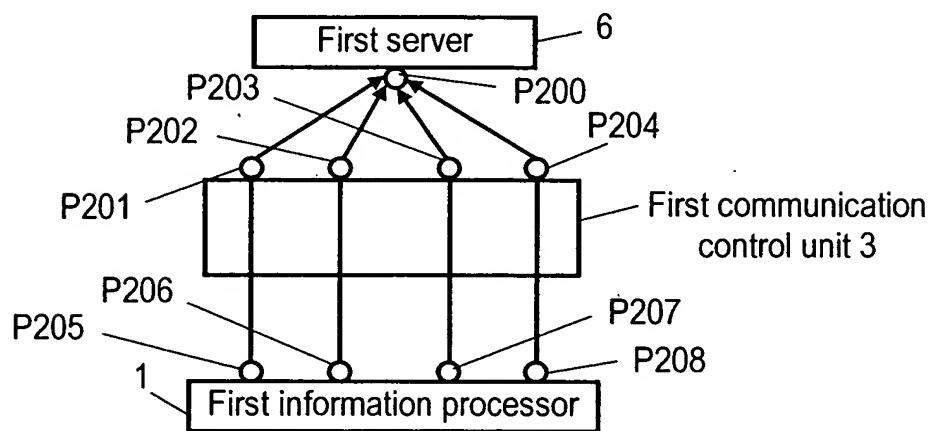


FIG. 12

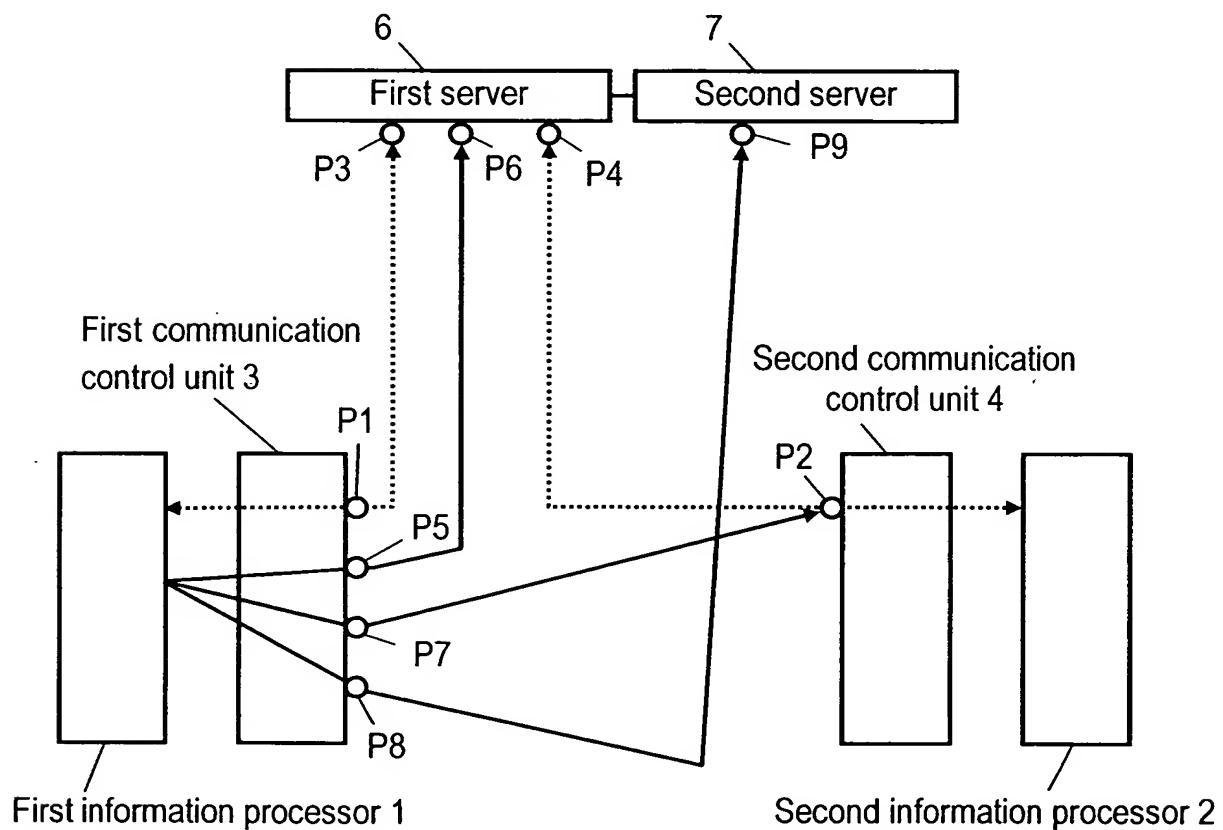
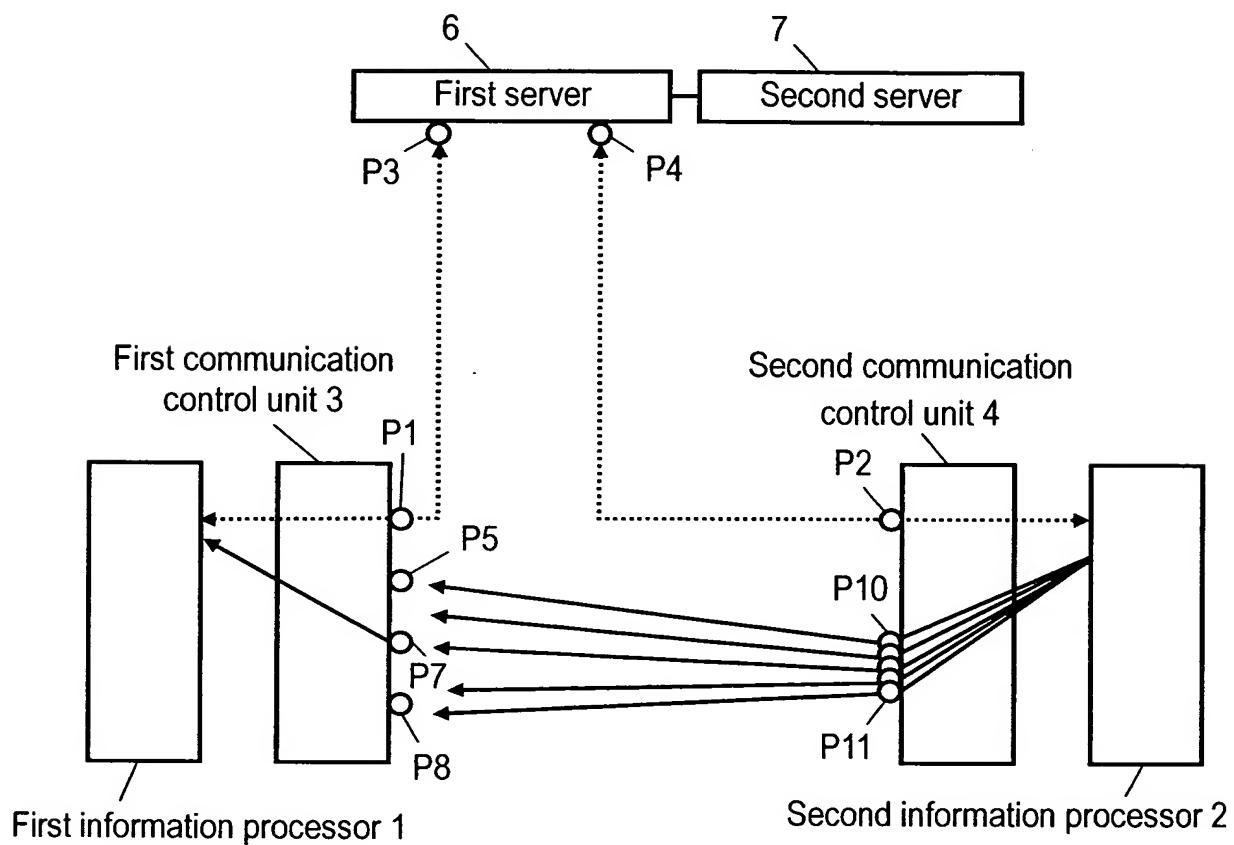
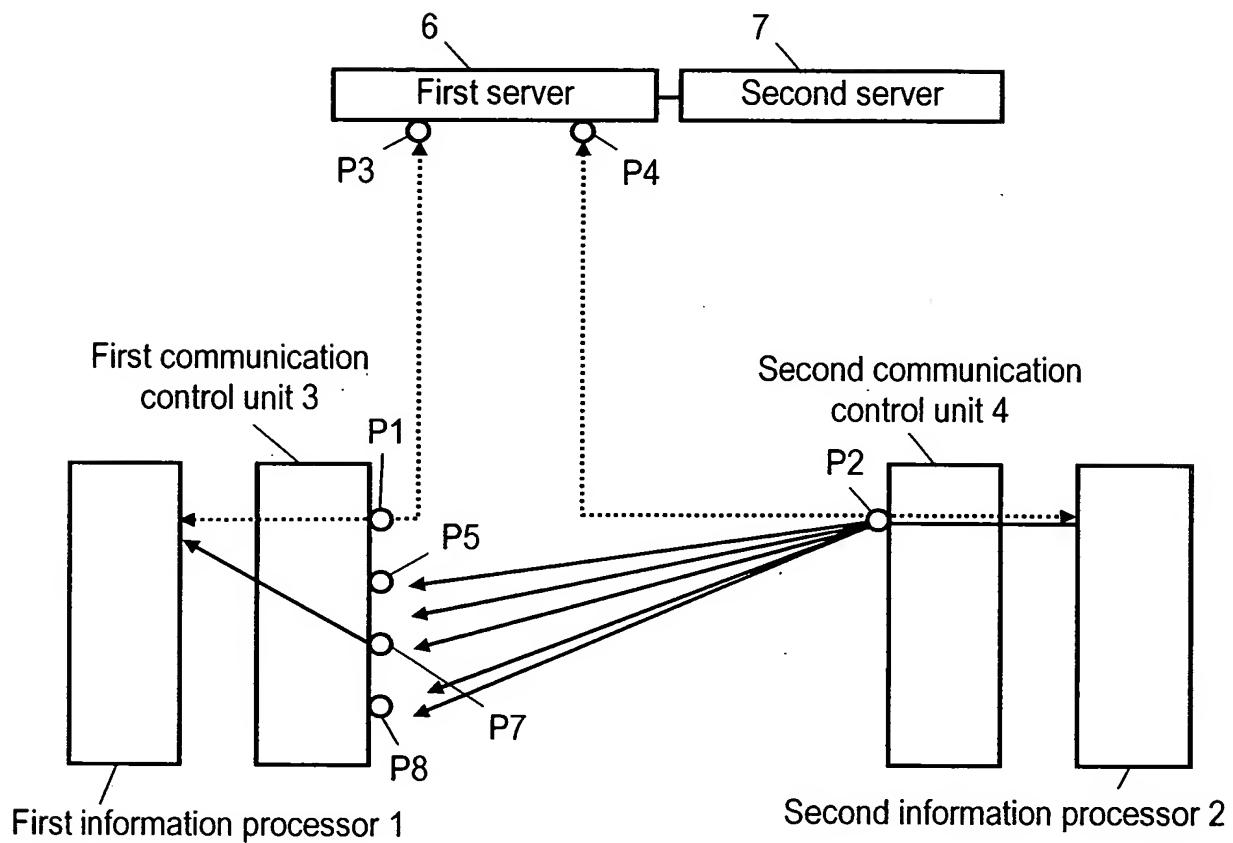


FIG. 13



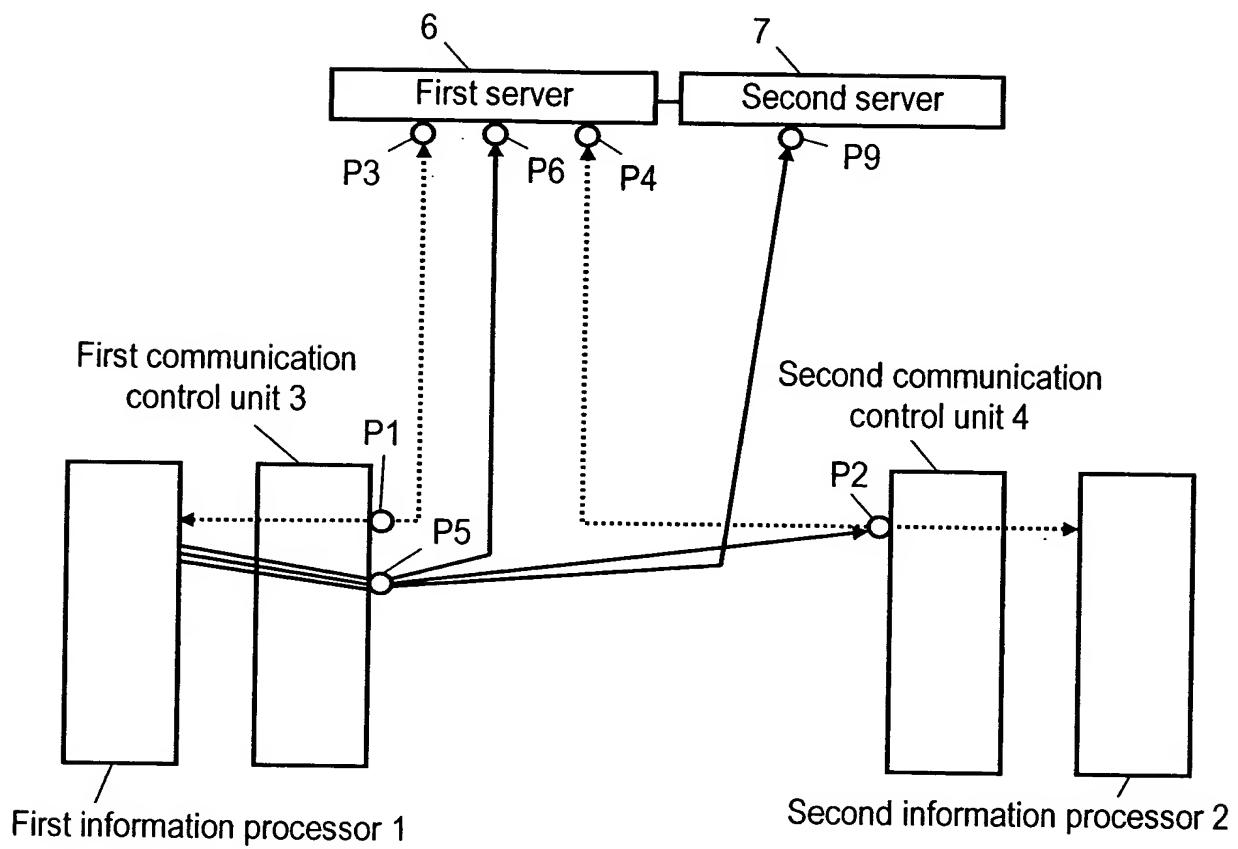
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FIG. 14



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FIG. 15



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FIG. 16

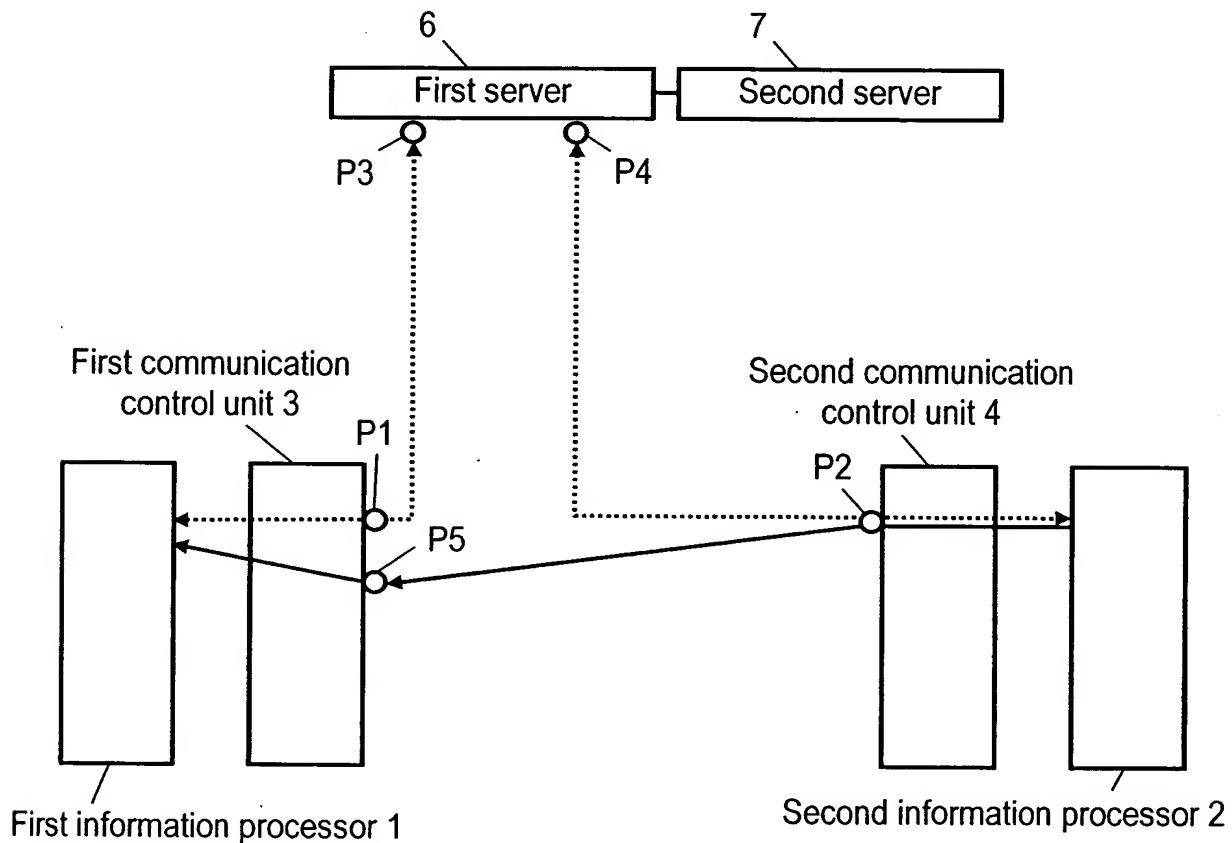


FIG. 17

Reception side \ Transmission side	Full Cone NAT	R NAT	PR NAT	AS NAT	PS NAT
Full Cone NAT	O	O	O	O	O
R NAT	O	O	O	O	O
PR NAT	O	O	O	Δ	Δ
AS NAT	O	O	O	O	O
PS NAT	O	O	O	Δ	×

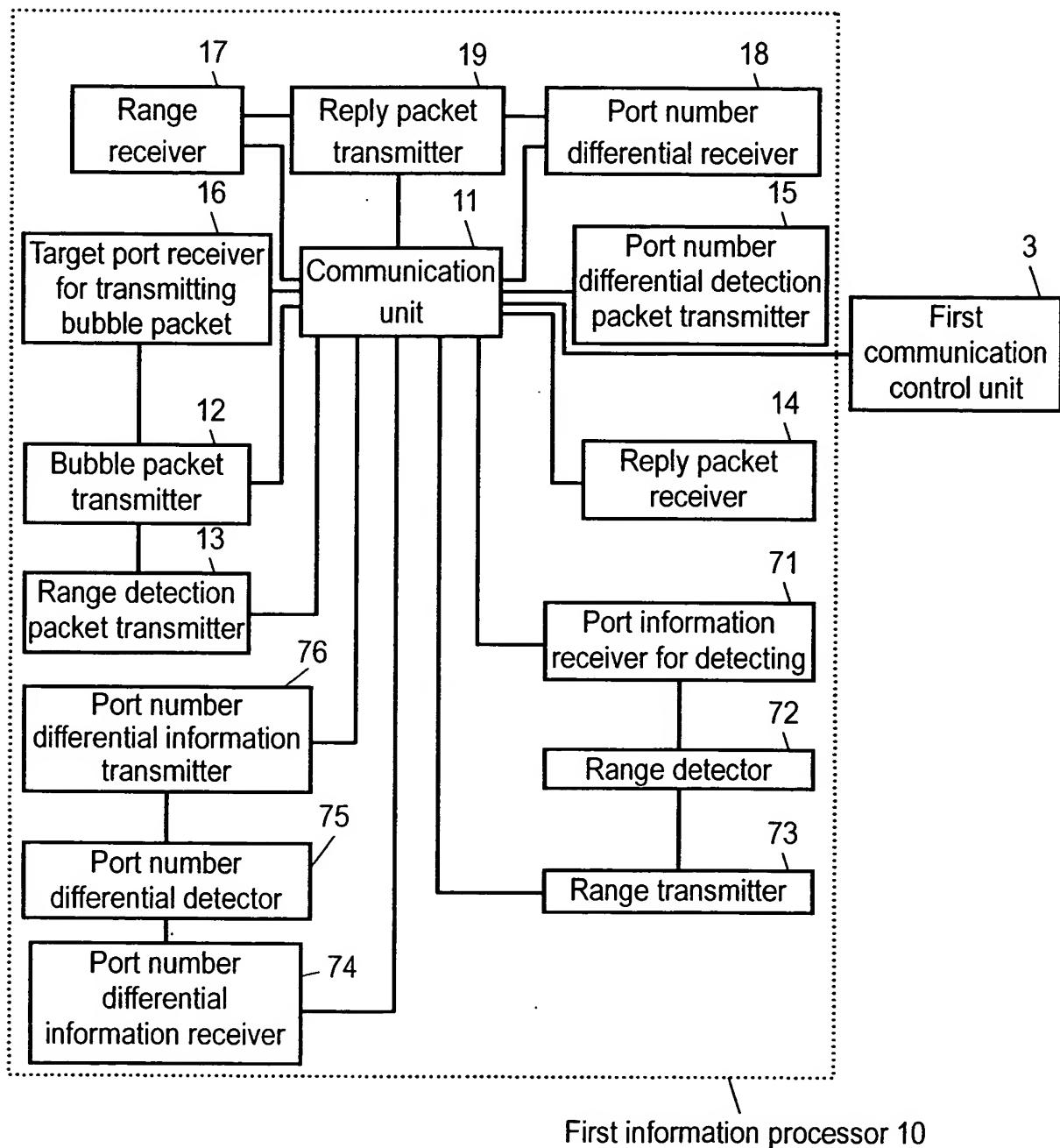
O : Connection enabled

Δ : Connection enabled by exchanging transmission and reception

× : Connection disabled

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FIG. 18



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FIG. 19

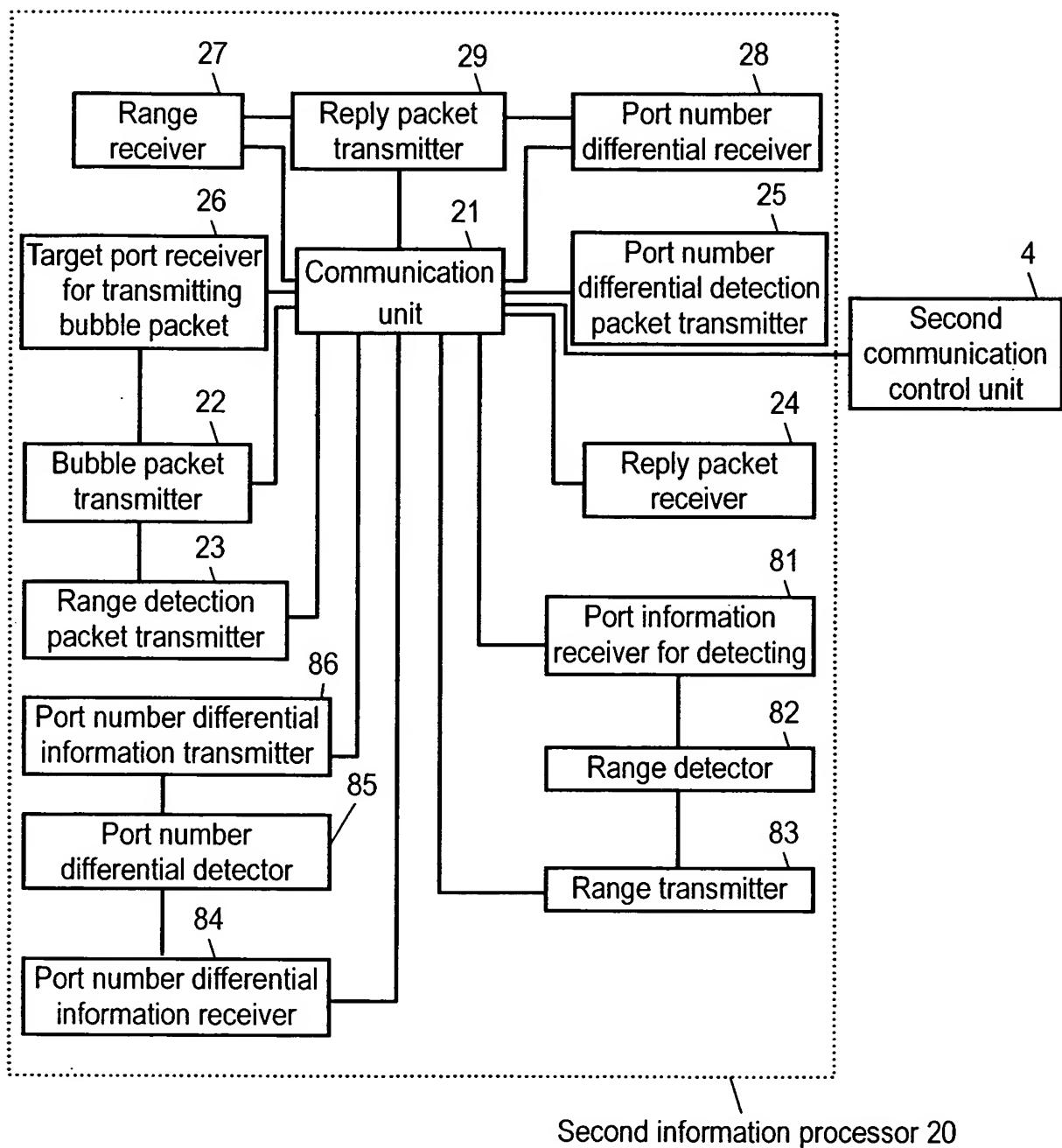


FIG. 20

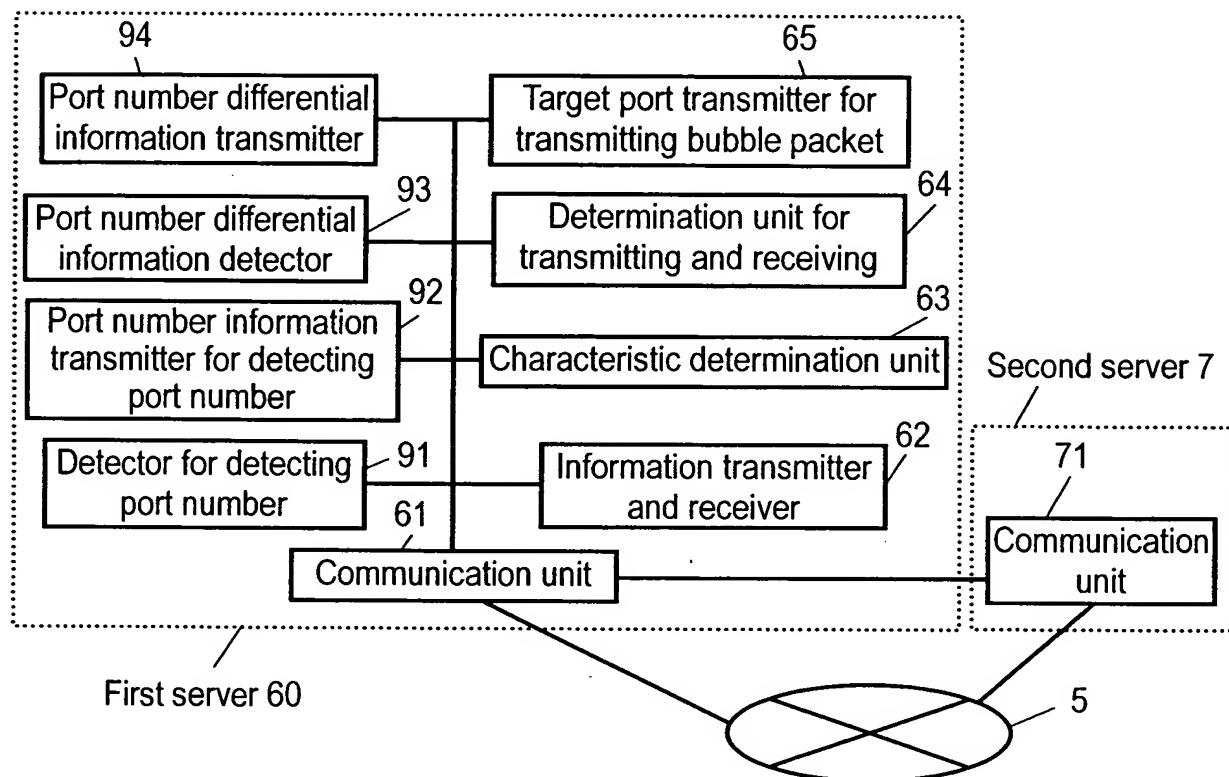
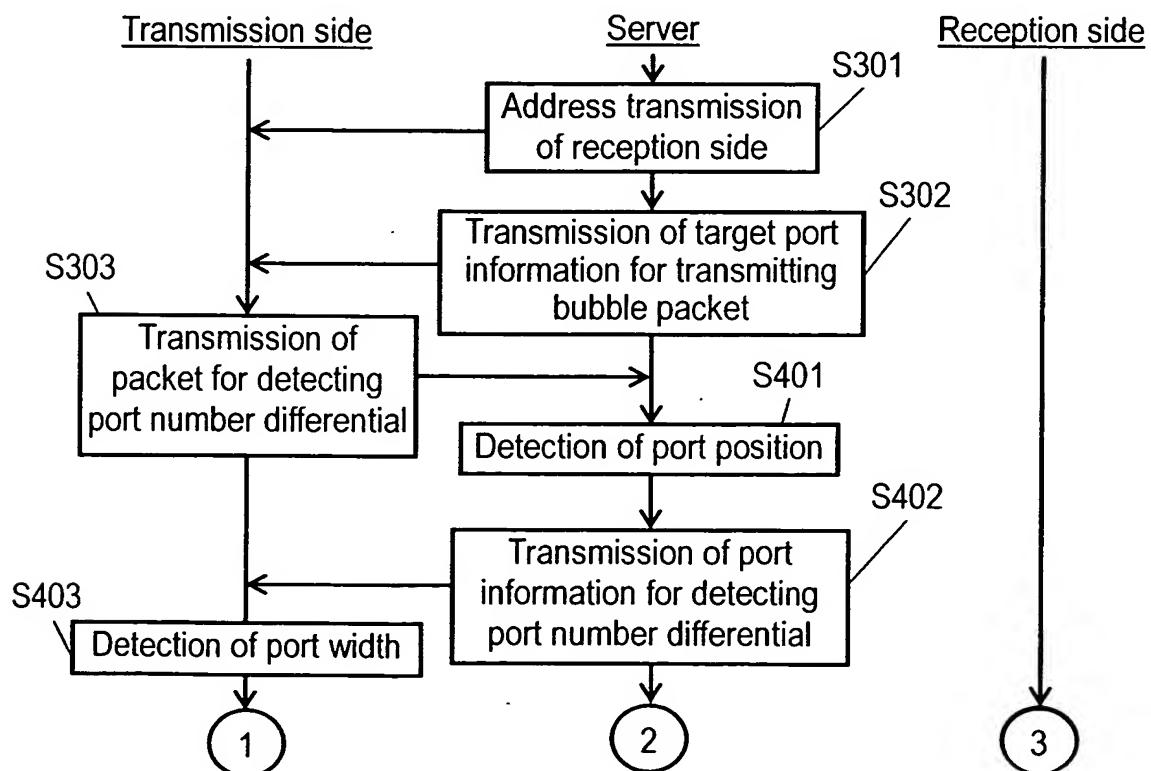
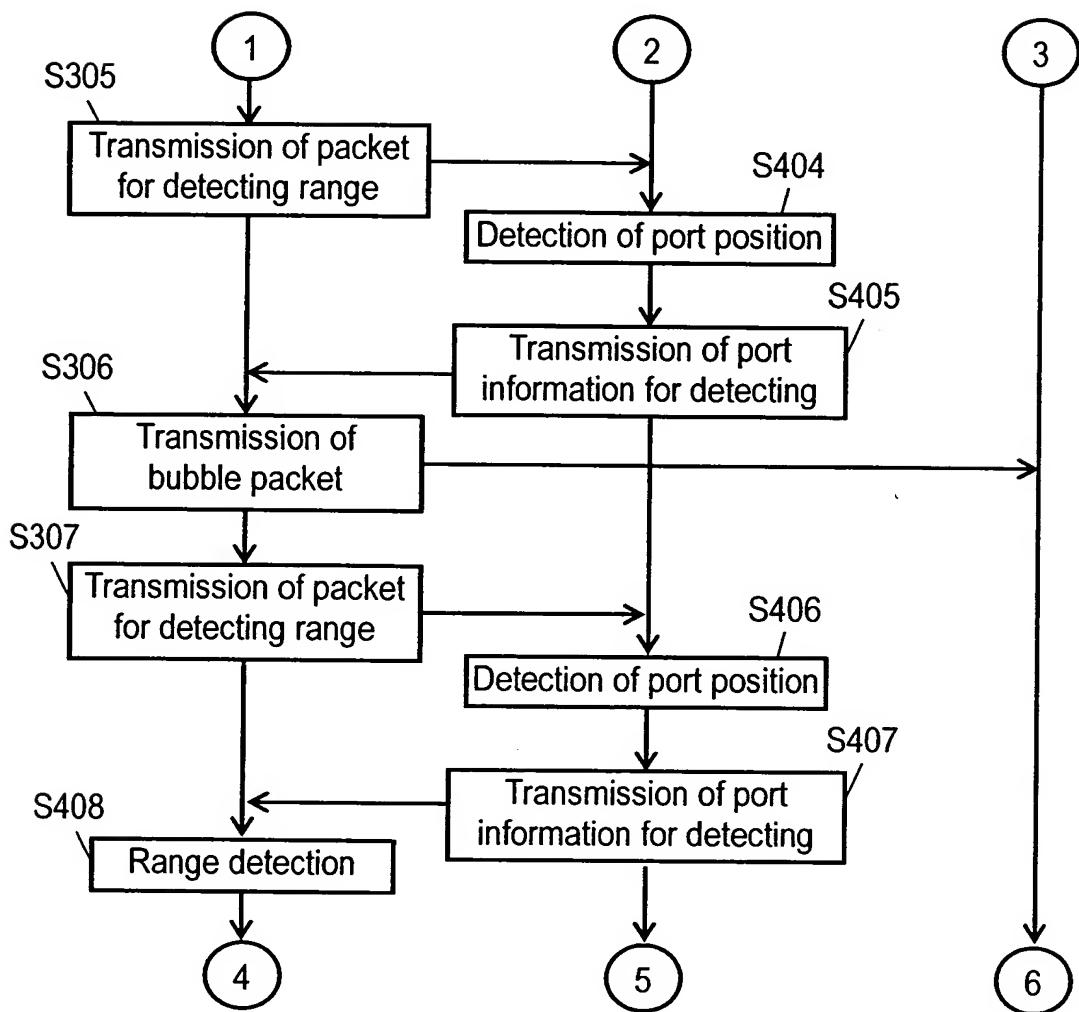


FIG. 21



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FIG. 22



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FIG. 23

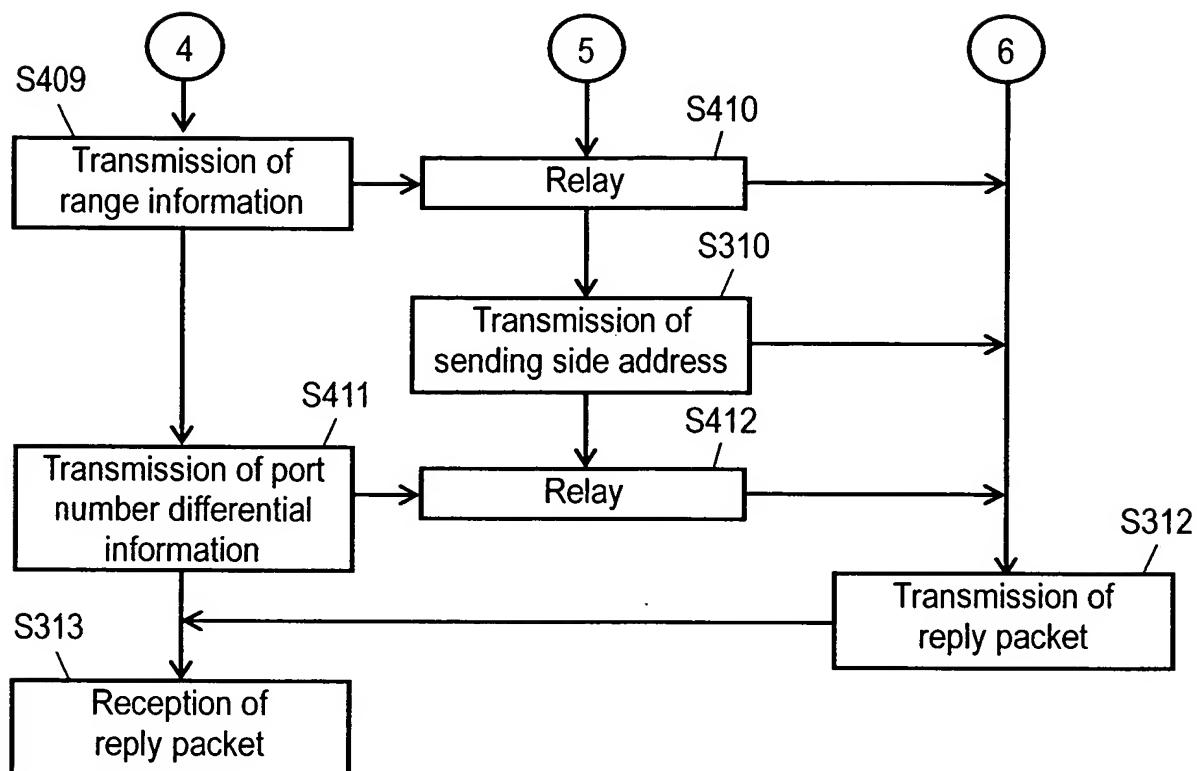
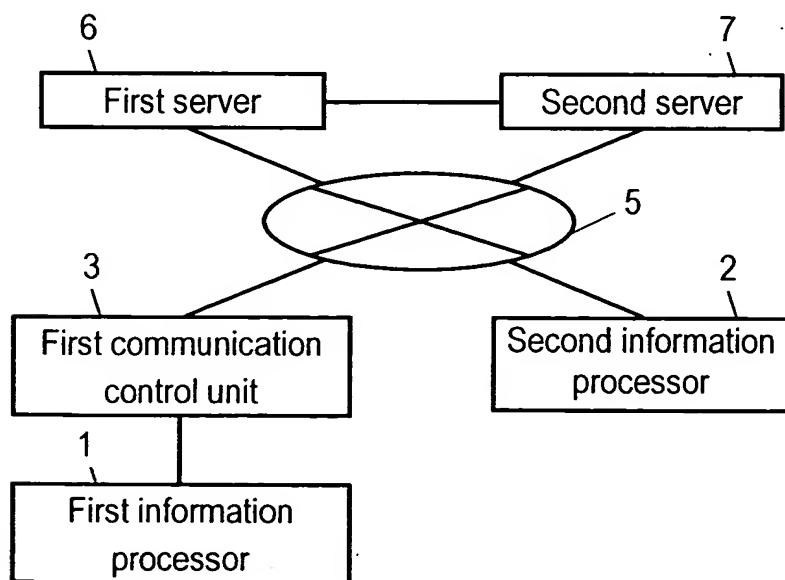
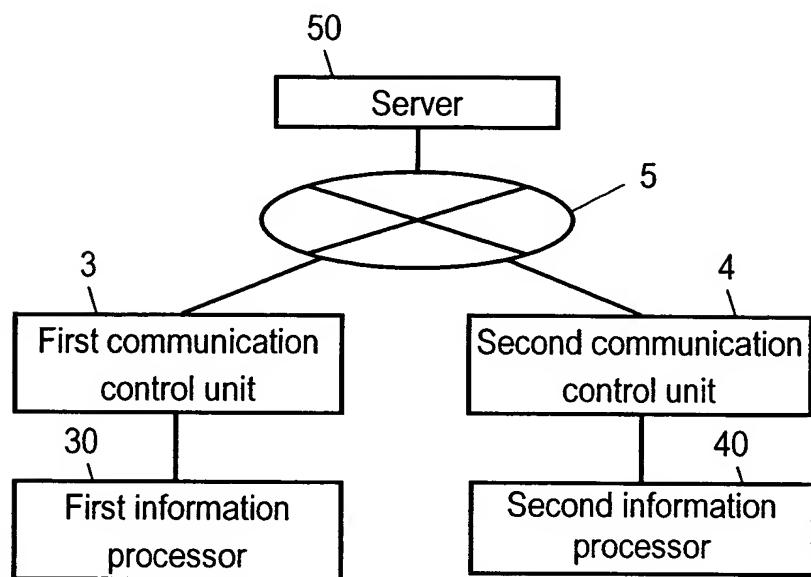


FIG. 24



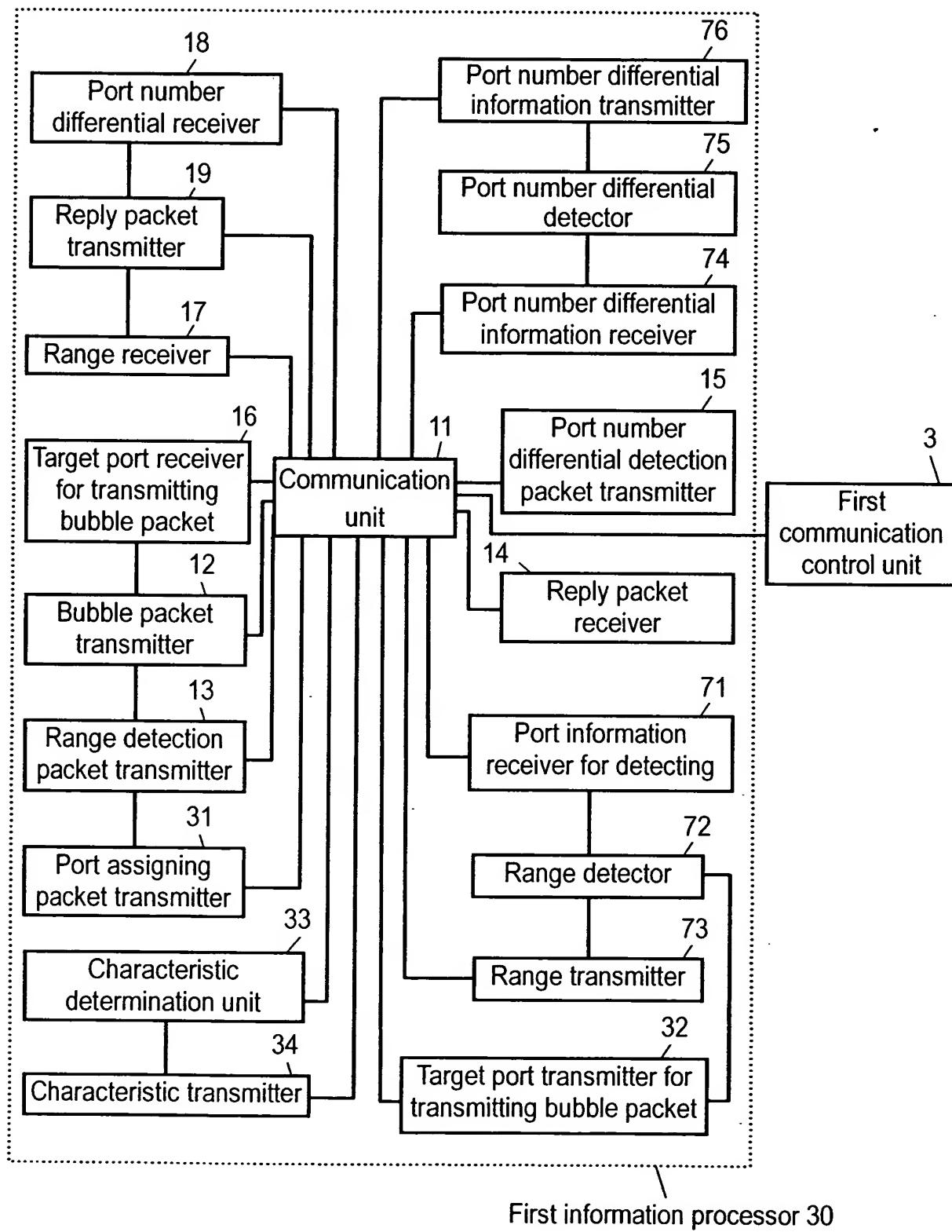
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FIG. 25



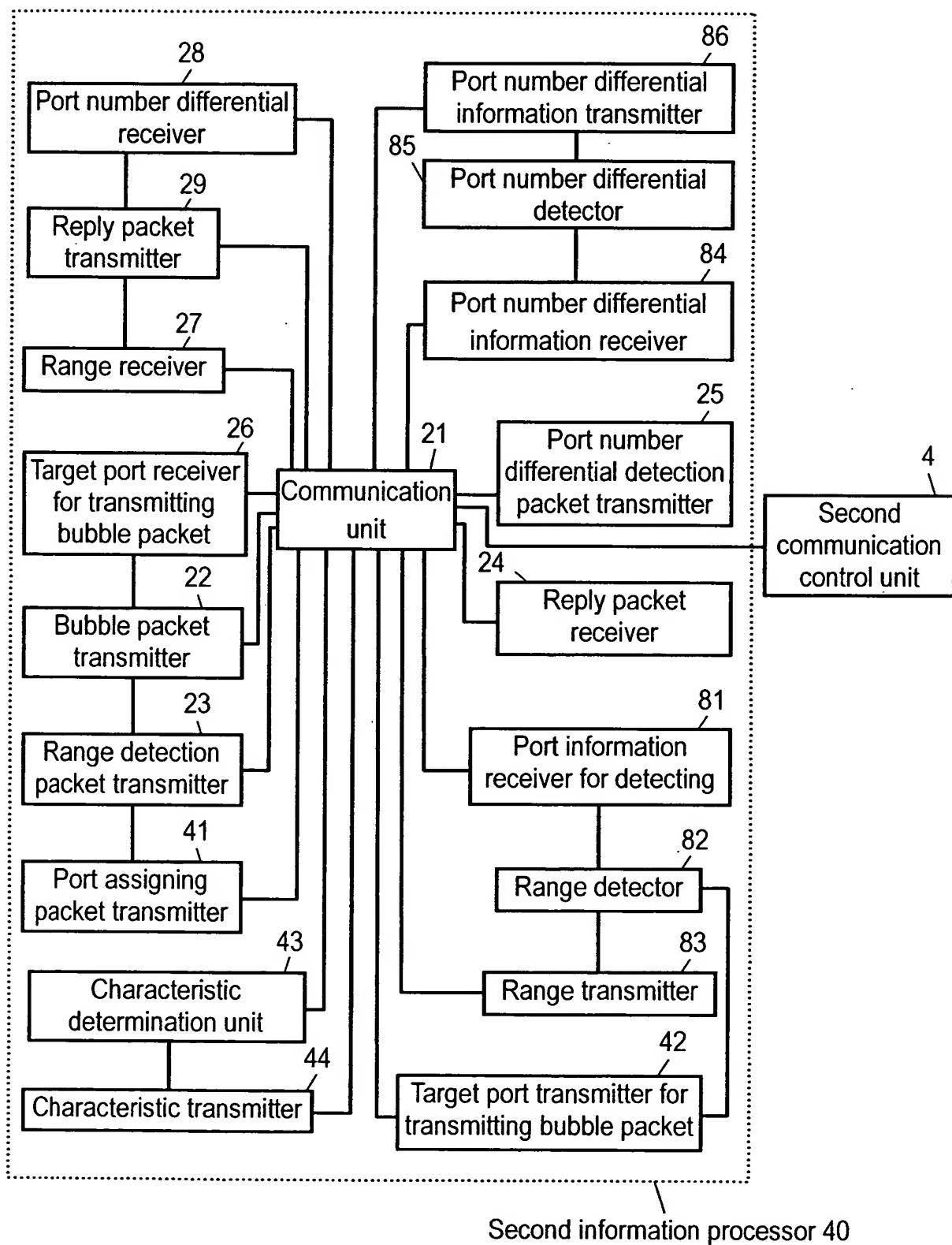
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FIG. 26



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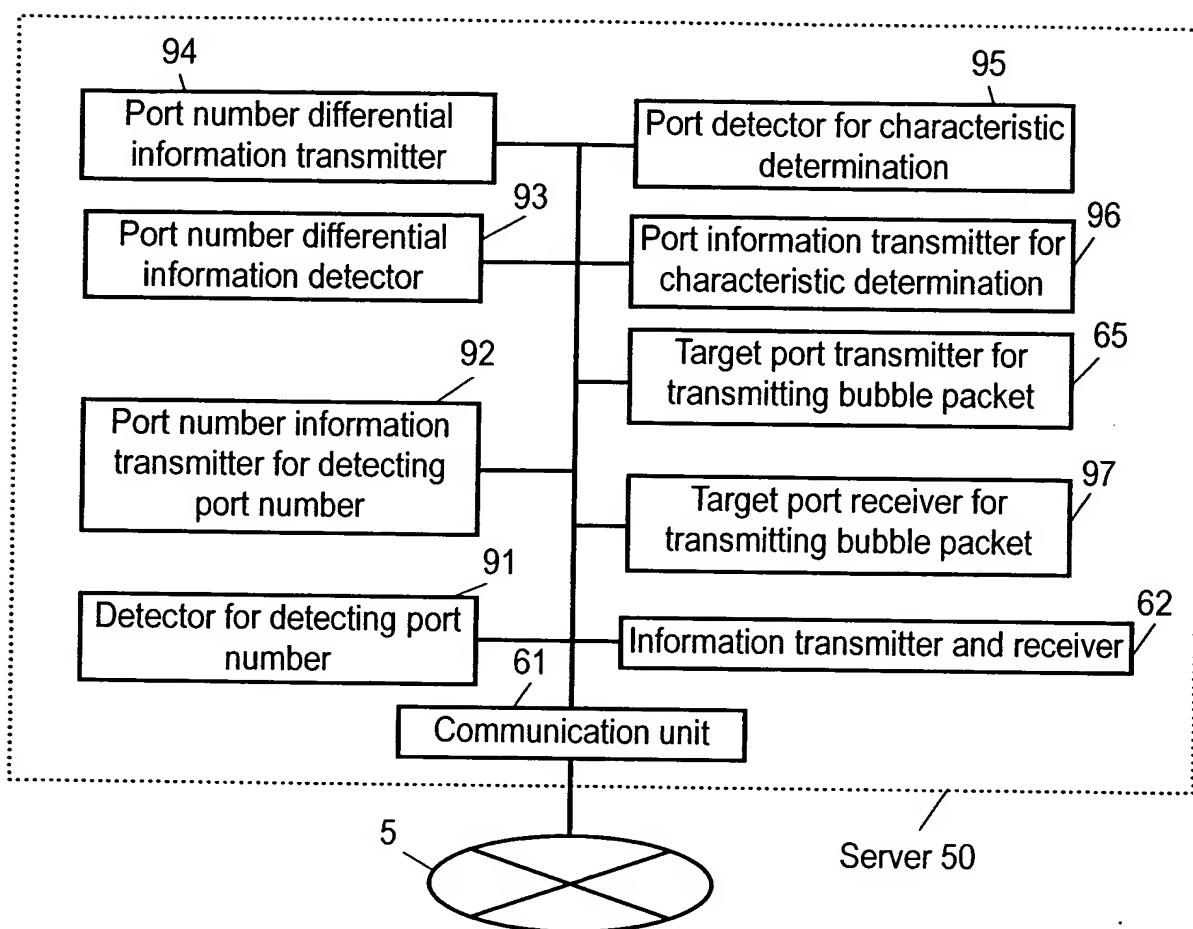
FIG. 27



Second information processor 40

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FIG. 28



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FIG. 29

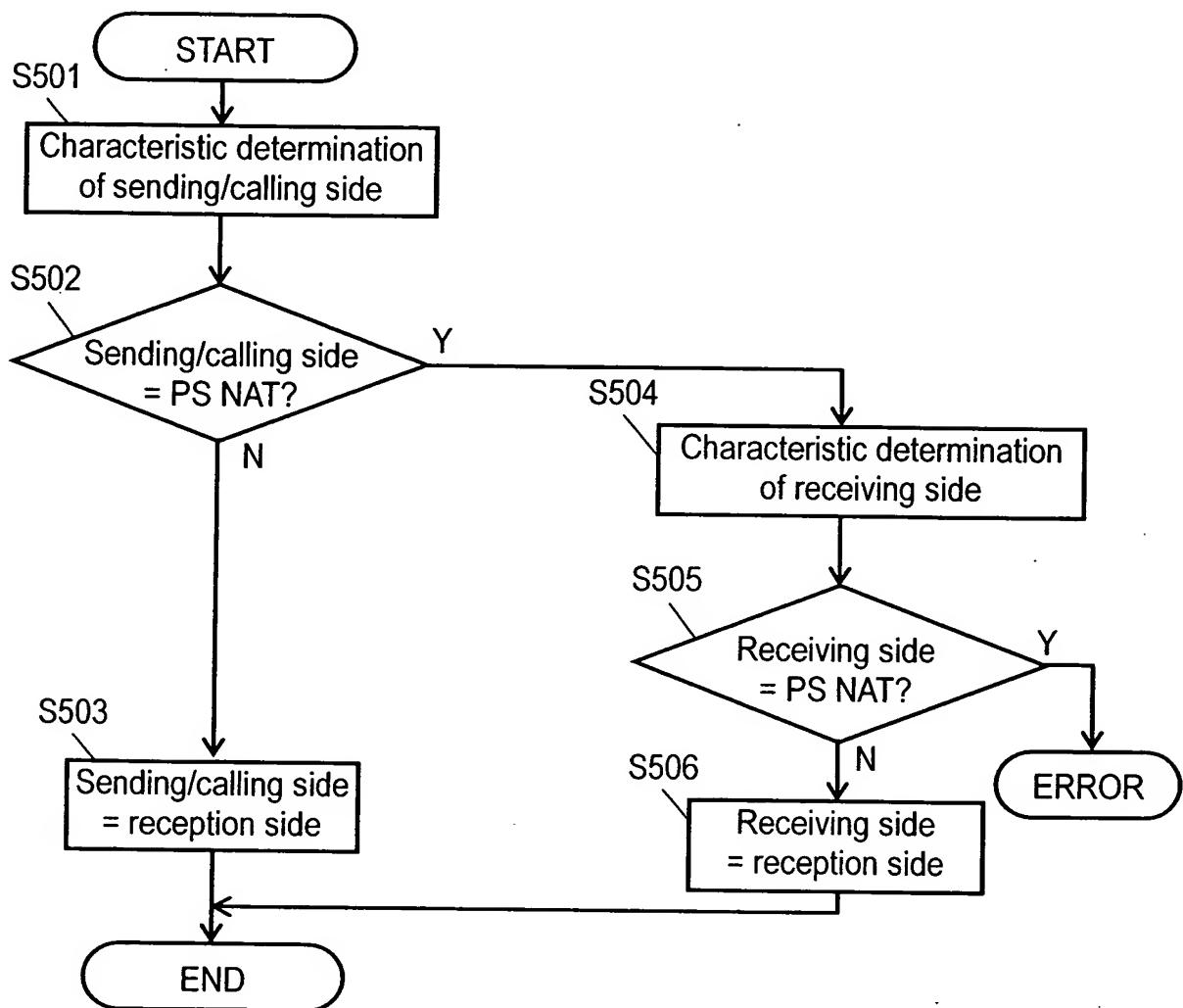
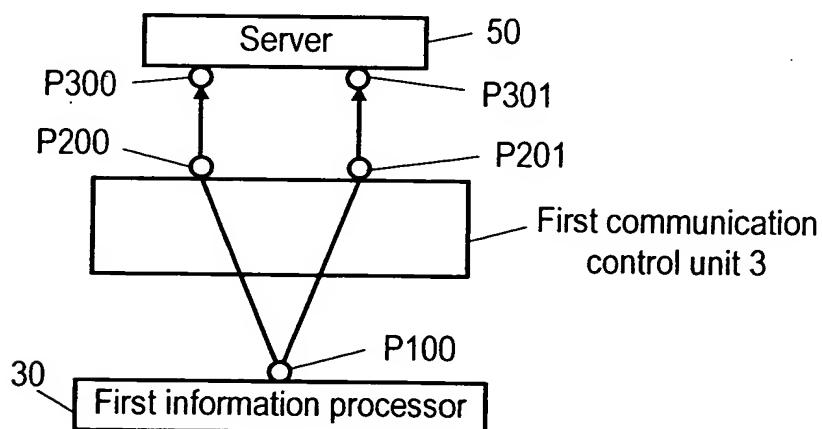


FIG. 30



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FIG. 31

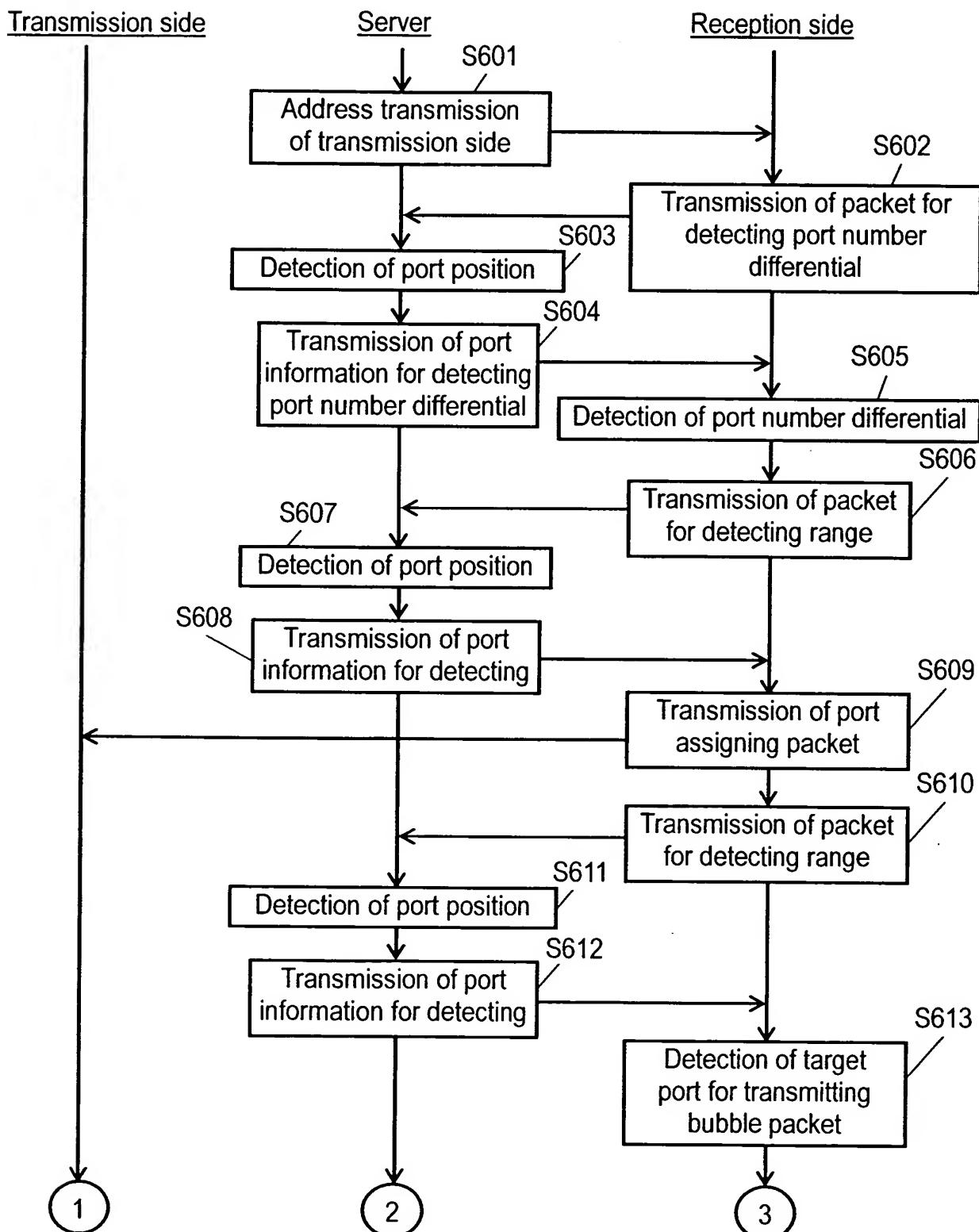
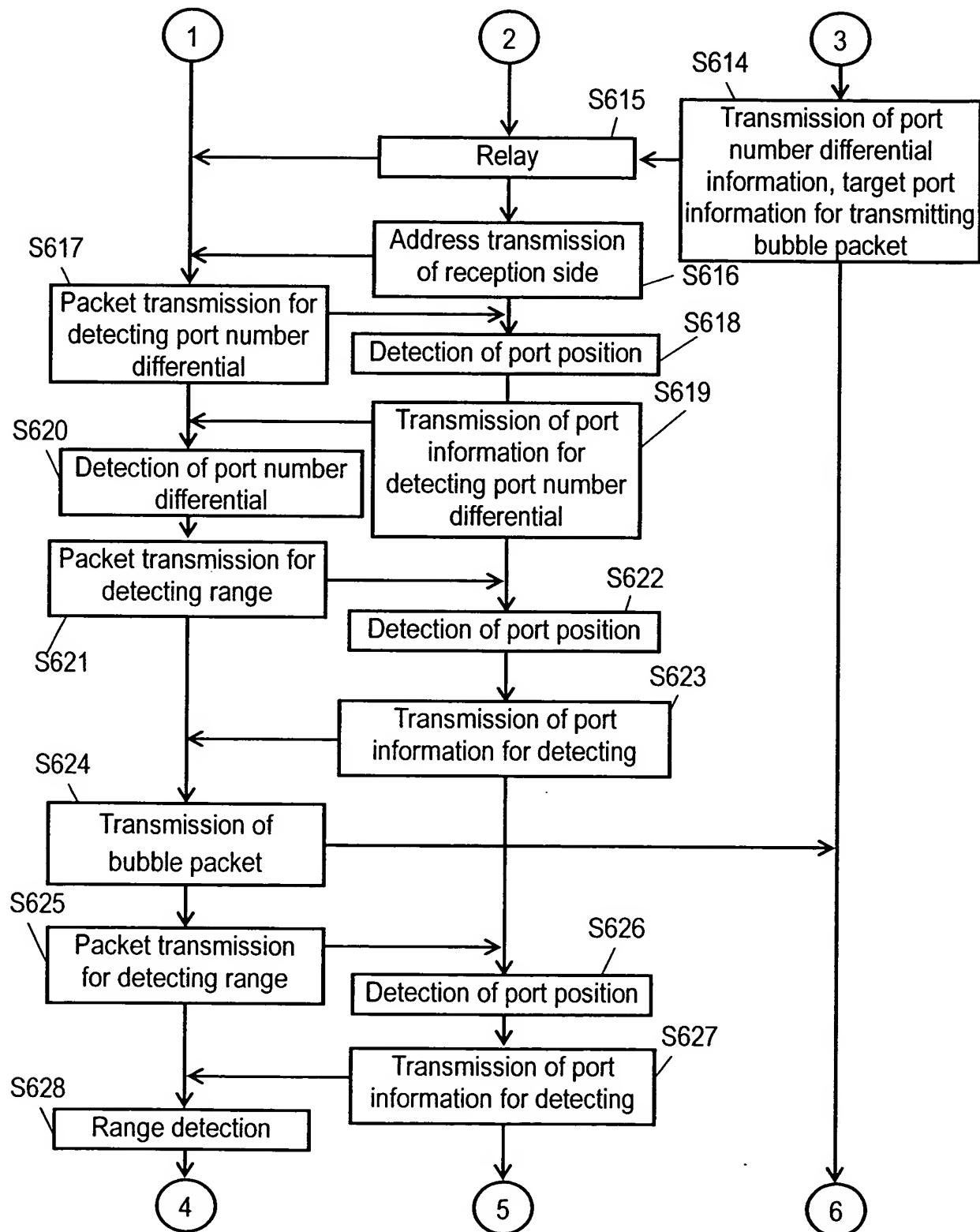


FIG. 32



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FIG. 33

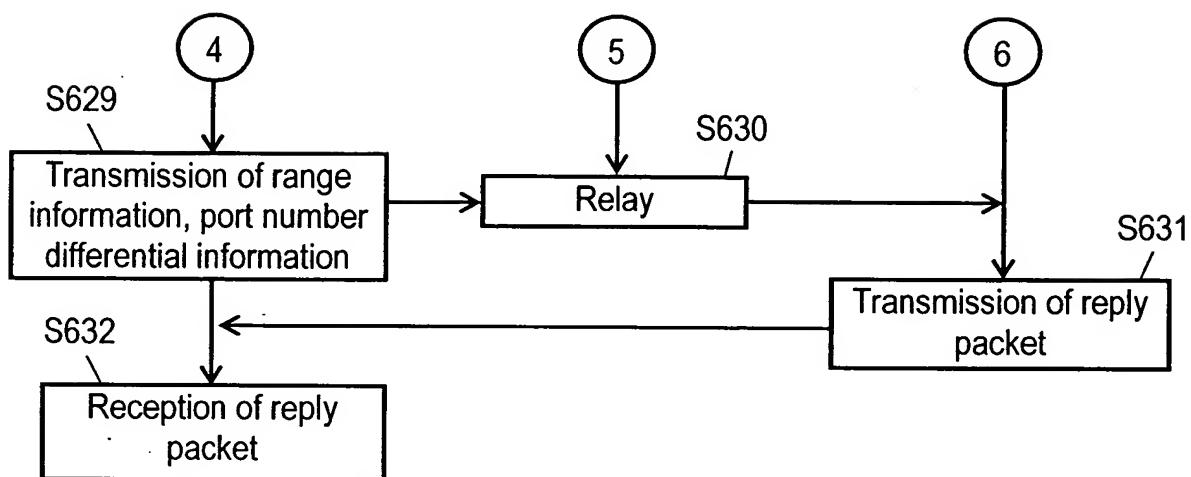
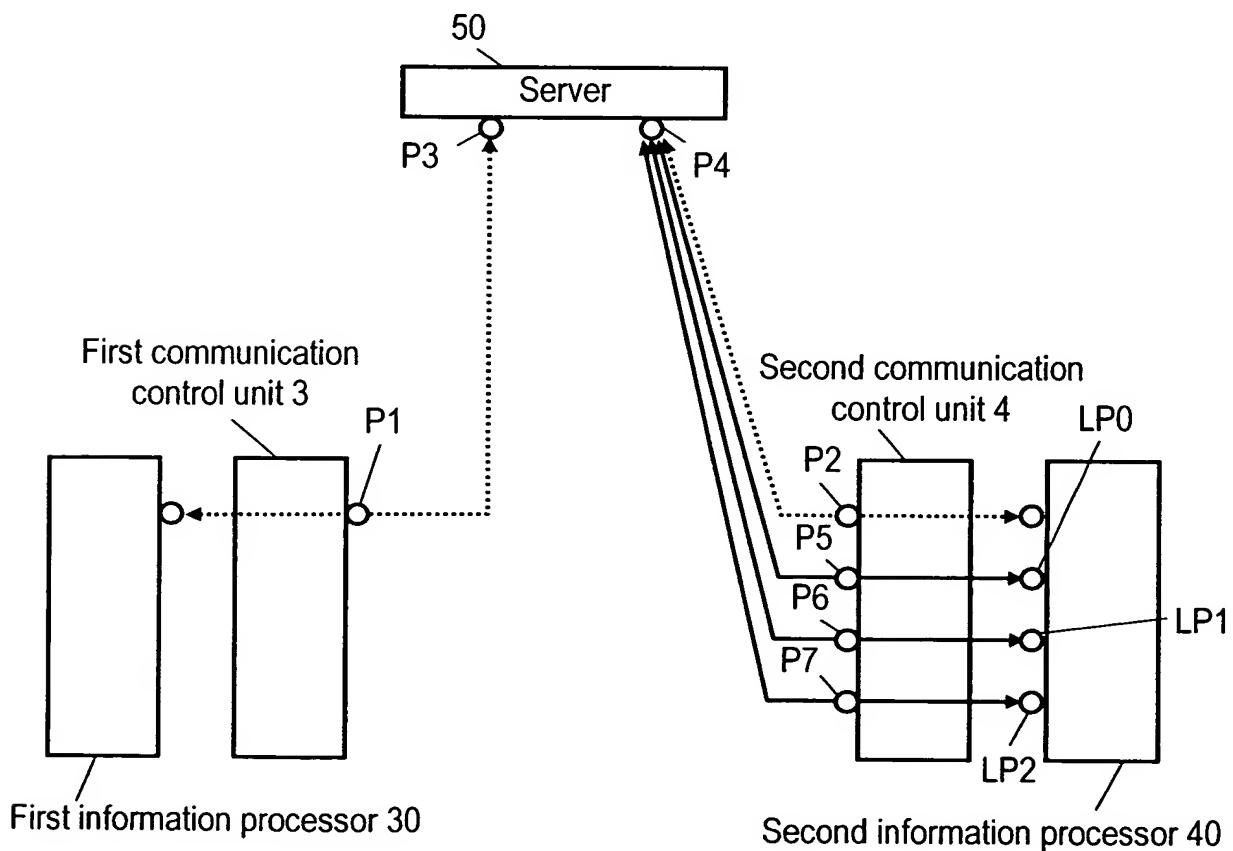


FIG. 34



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FIG. 35

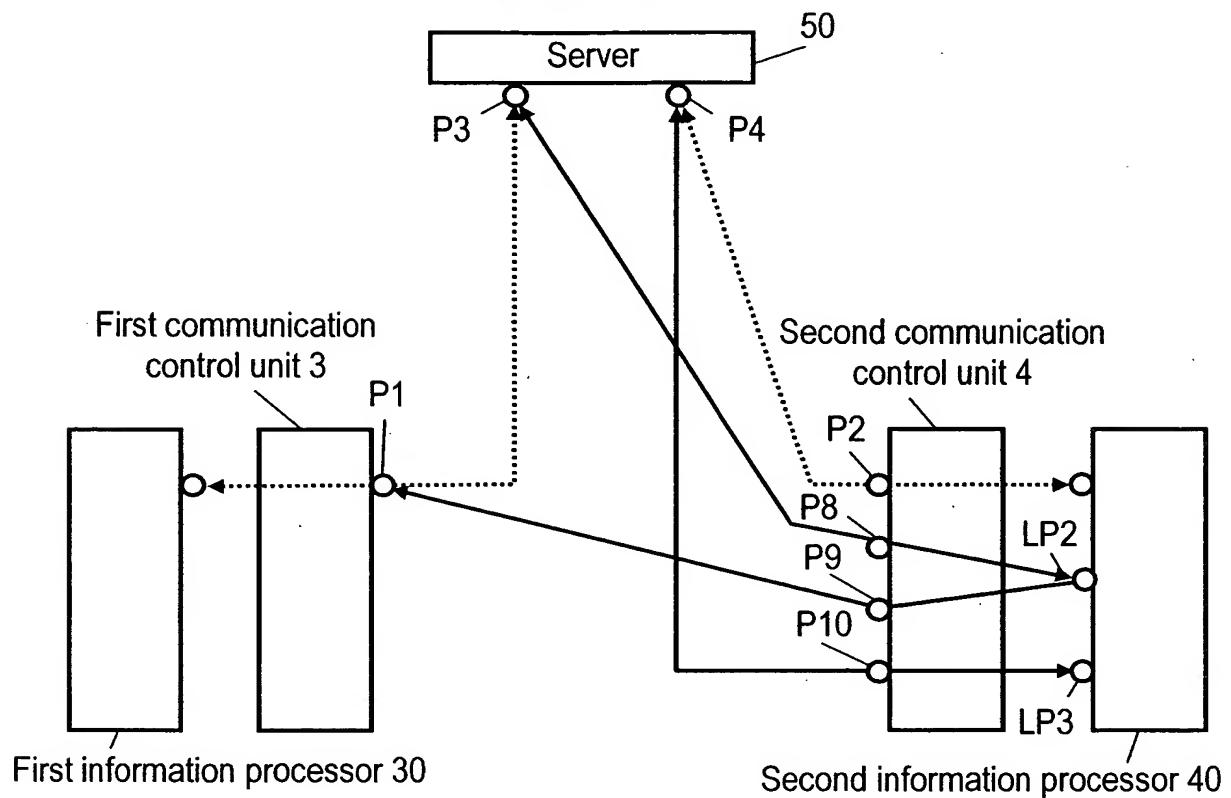
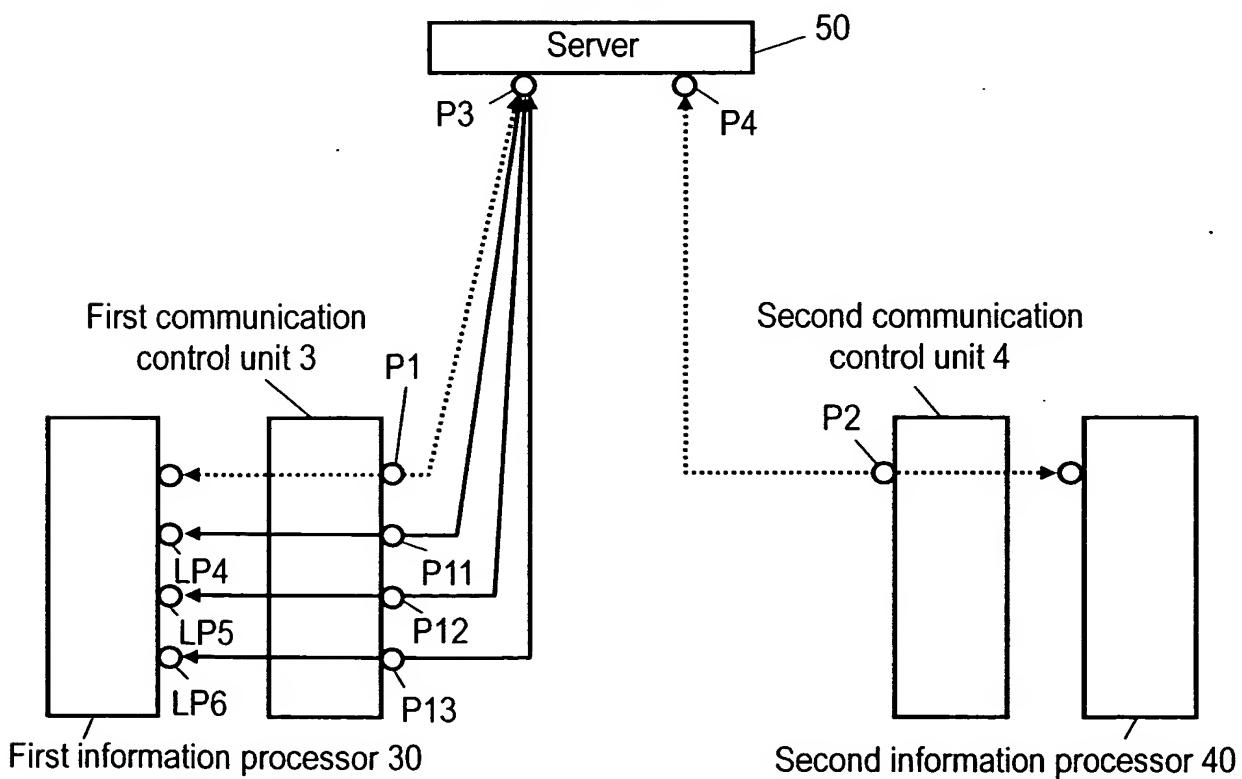


FIG. 36



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FIG. 37

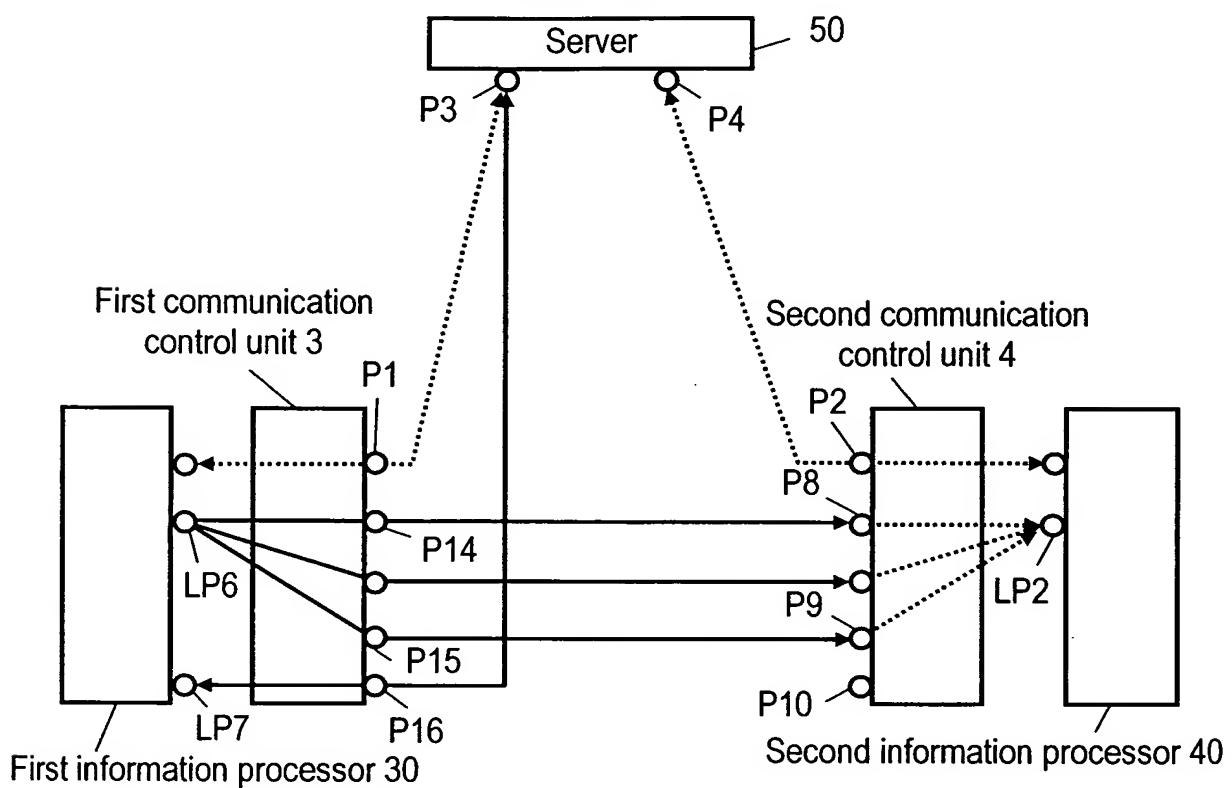
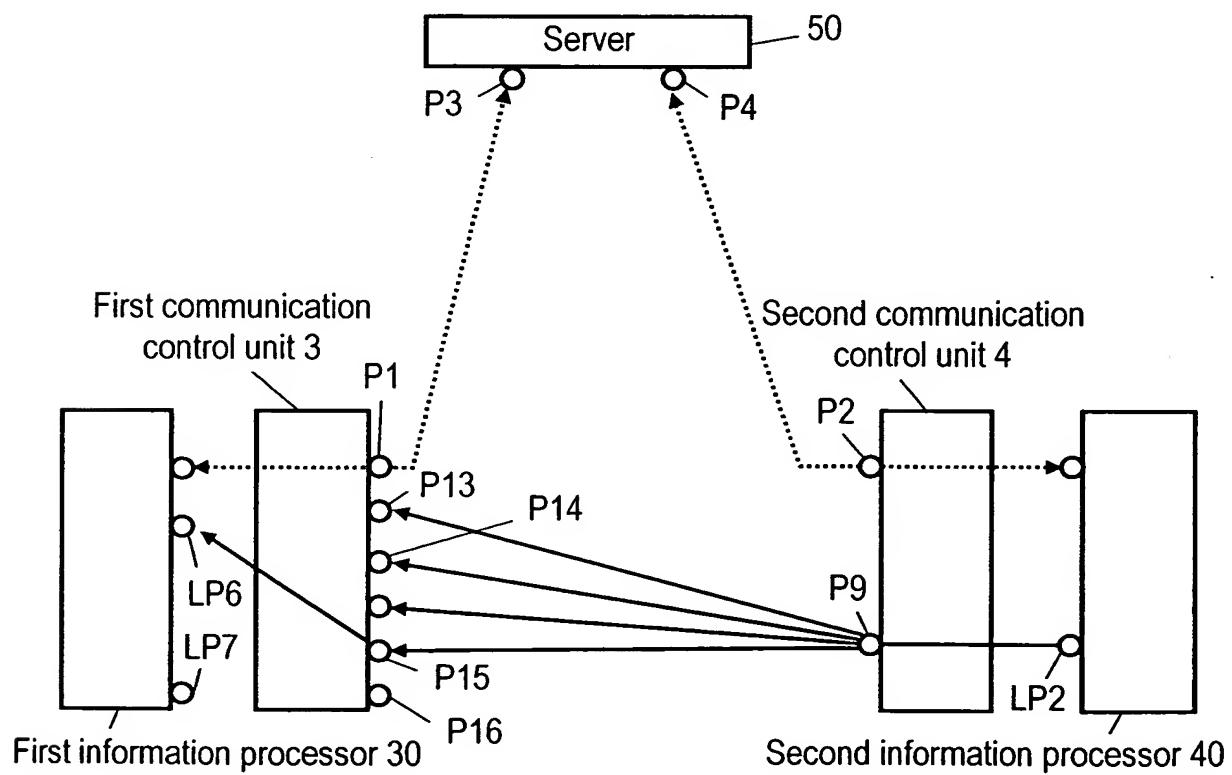


FIG. 38



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FIG. 39

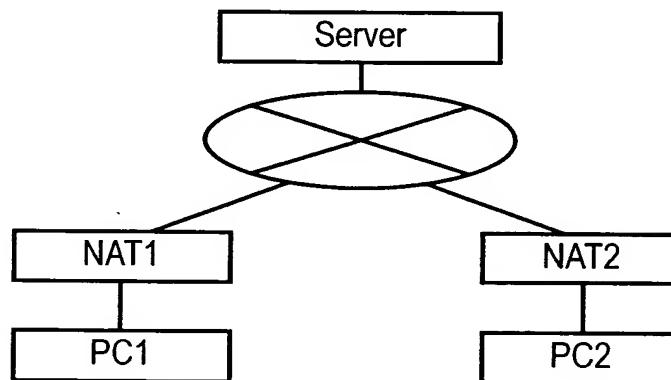
Reception side Transmis- sion side\	Full Cone NAT	R NAT	PR NAT	AS NAT	PS NAT
Full Cone NAT	O	O	O	O	Δ
R NAT	O	O	O	O	Δ
PR NAT	O	O	O	O	Δ
AS NAT	O	O	O	O	Δ
PS NAT	O	O	O	O	×

O : Connection enabled

Δ : Connection enabled by exchanging transmission and reception

× : Connection disabled

FIG. 40



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FIG. 41

Reception side Trans-mission side	Full Cone NAT	R NAT	PR NAT	AS NAT	PS NAT
Full Cone NAT	O * 1	O * 1	O * 1	O * 2	O * 2
R NAT	O * 1	O * 1	O * 1	O * 2	O * 2
PR NAT	O * 1	O * 1	O * 1	O * 3	O * 3
AS NAT	O * 1	O * 1	O * 3	O * 3	O * 3
PS NAT	O * 1	O * 1	x	x	x

O : Connection enabled

x : Connection disabled